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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Robert F. Wallace
Assignee: SanDisk Corporation
Title: Semiconductor Package Using Terminals Formed on a Conductive Layer of a Circuit Board
Application No.: 09/866,022 Filing Date: May 24, 2001
Examiner: Potter, Roy Karl Group Art Unit: 2822
Docket No.: M-10238-2C US Conf. No.: 6172

San Francisco, California
February 27, 2003

Commissioner for Patents
Washington, D.C. 20231

RESPONSE TO OFFICE ACTION

Sir:

In response to the Office Action dated October 3, 2002, a Terminal Disclaimer is being filed herewith. Since this is believed to overcome the non-statutory double patenting rejection, allowance of the present application is in order.

However, it is noted that there is no record in the file of the undersigned attorney that the Information Disclosure Statement filed with the application has been considered. Therefore, a copy is being filed herewith. Consideration of each of the references listed on its 2 pages of Form PTO 1449 is respectfully requested. A return of the 1449s with the Examiner's initials is also solicited.

EXPRESS MAIL LABEL NO:

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Respectfully submitted,

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